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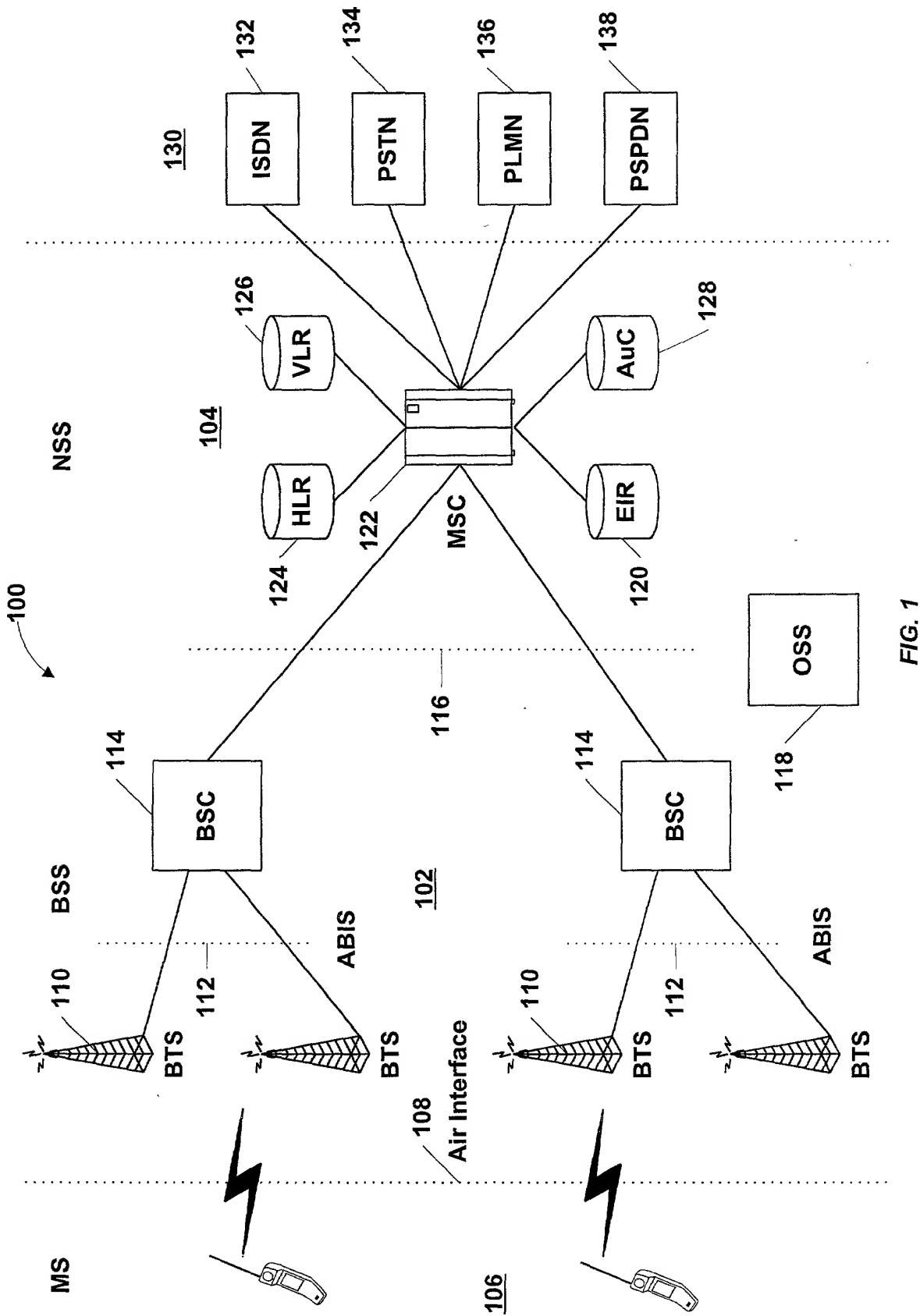


FIG. 1

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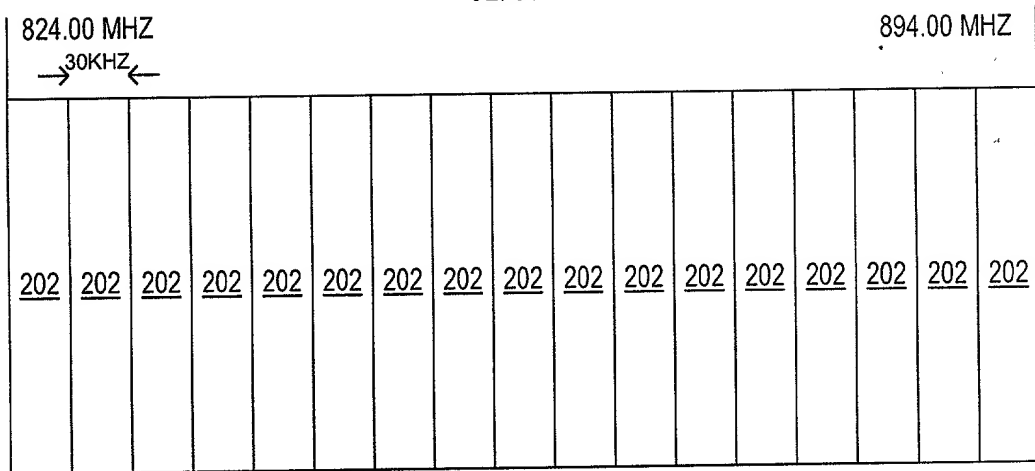


FIG. 2A
FDMA

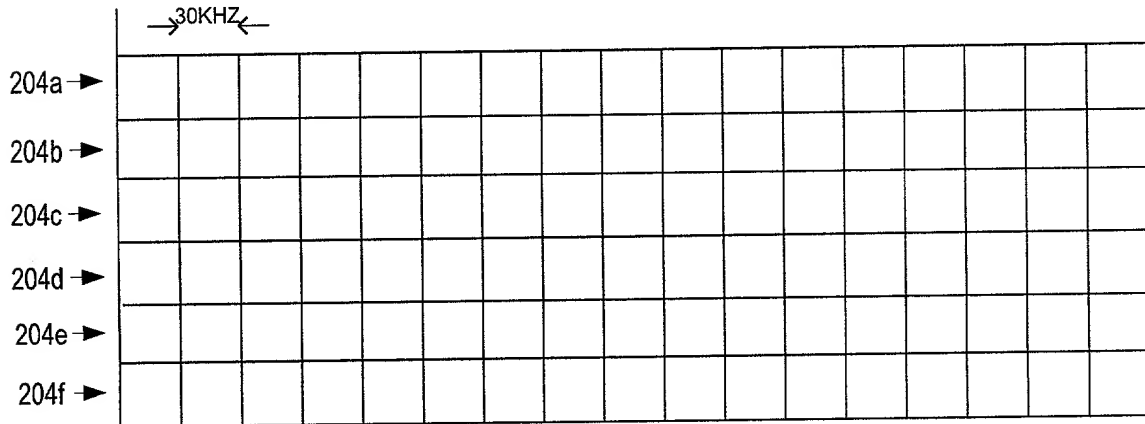


FIG. 2B
TDMA

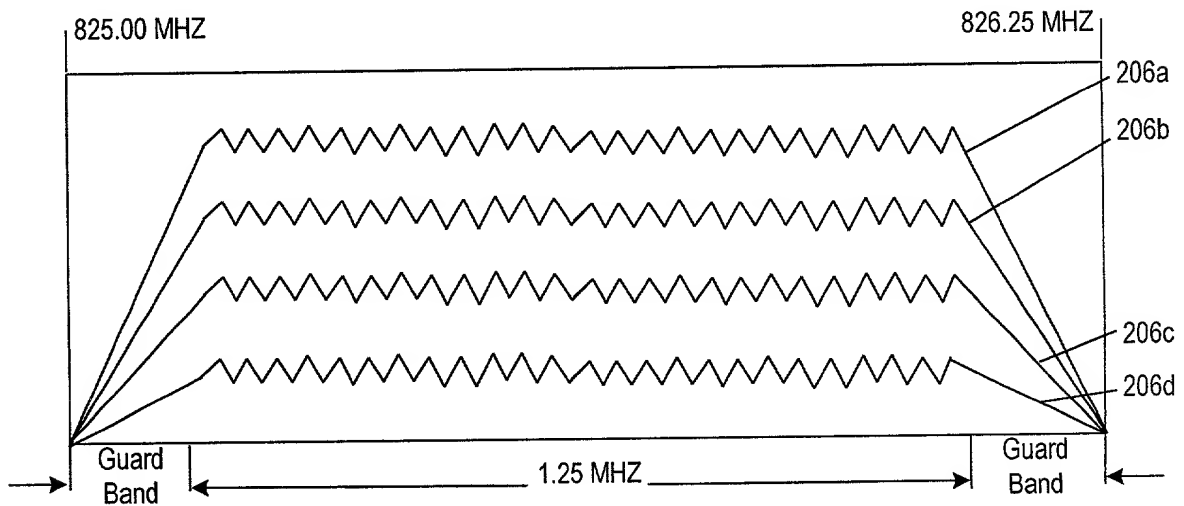


FIG. 2C
CDMA

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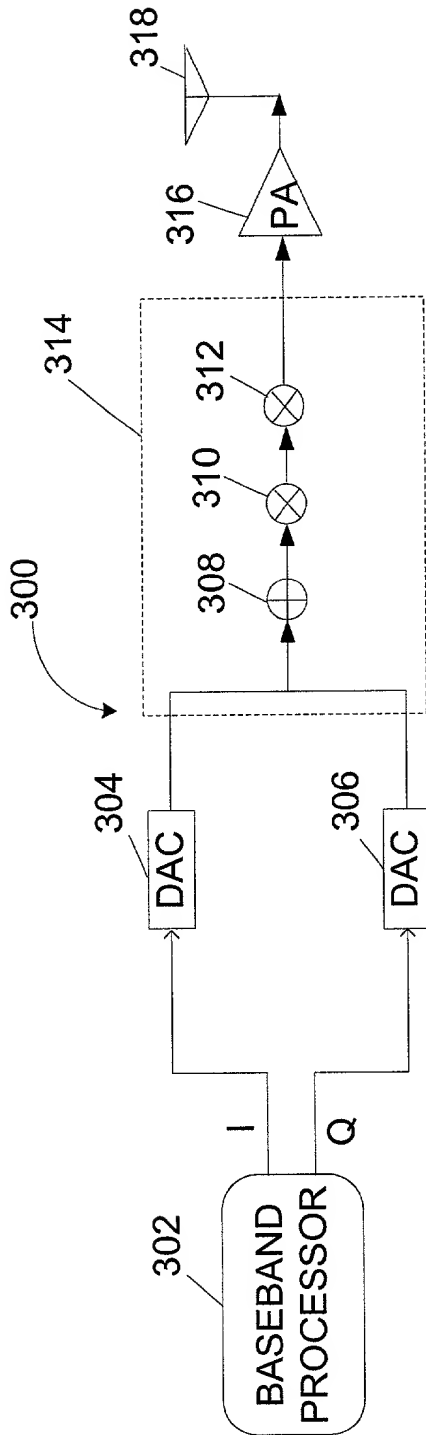


FIG. 3A

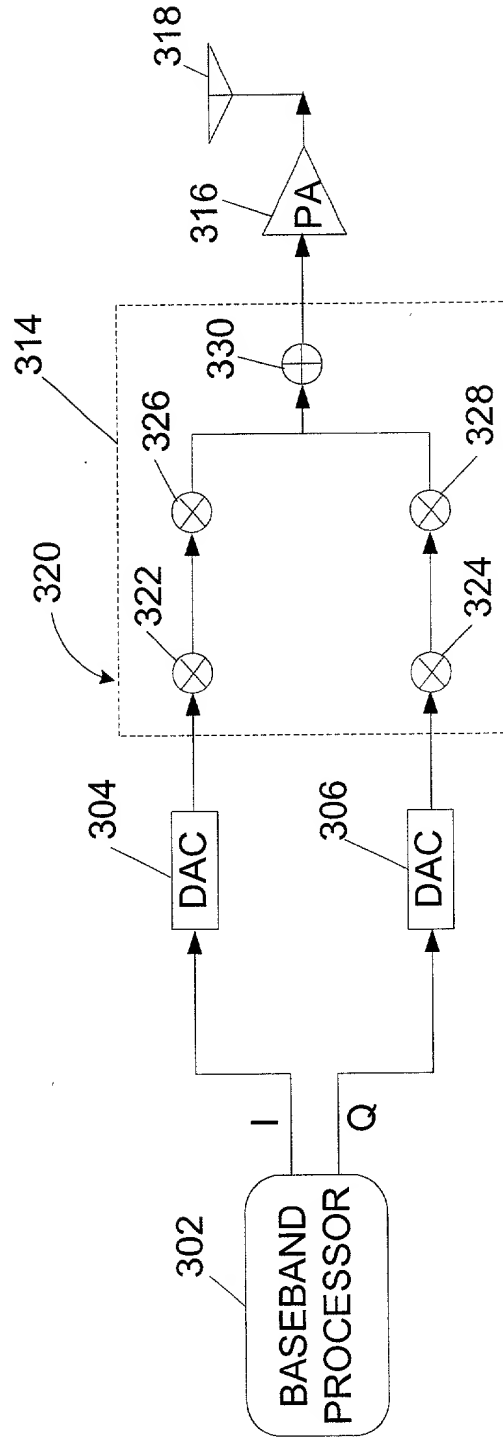


FIG. 3B

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Symbol State	I	Q	Symbol Bit Pattern	Phase State
1	1	1	00	45°
2	-1	1	10	135°
3	-1	-1	11	225°
4	1	-1	01	315°

FIG. 4

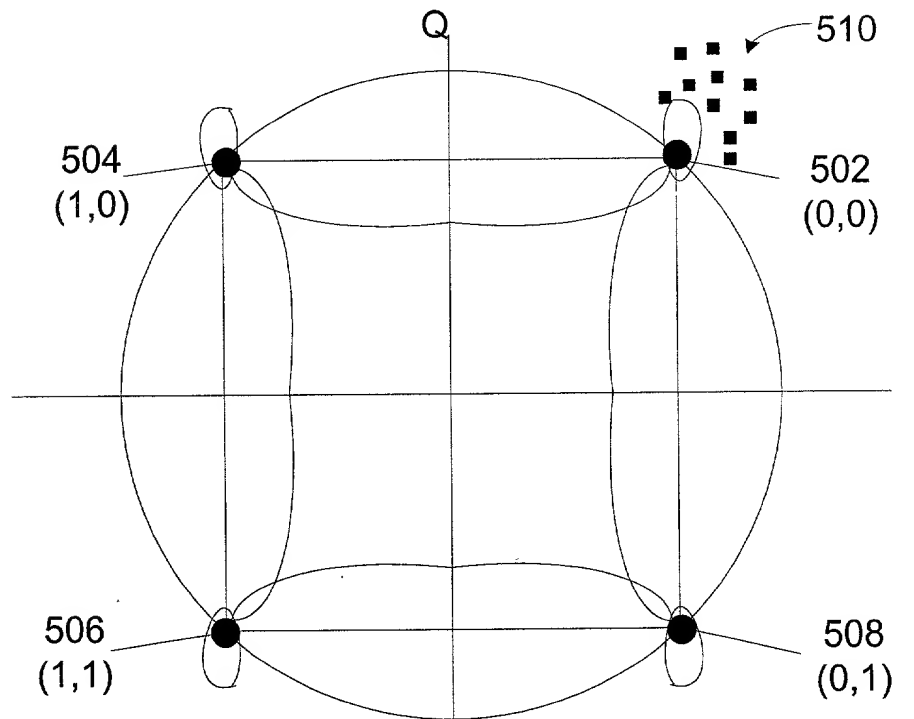


FIG. 5

FIG. 40 00000000

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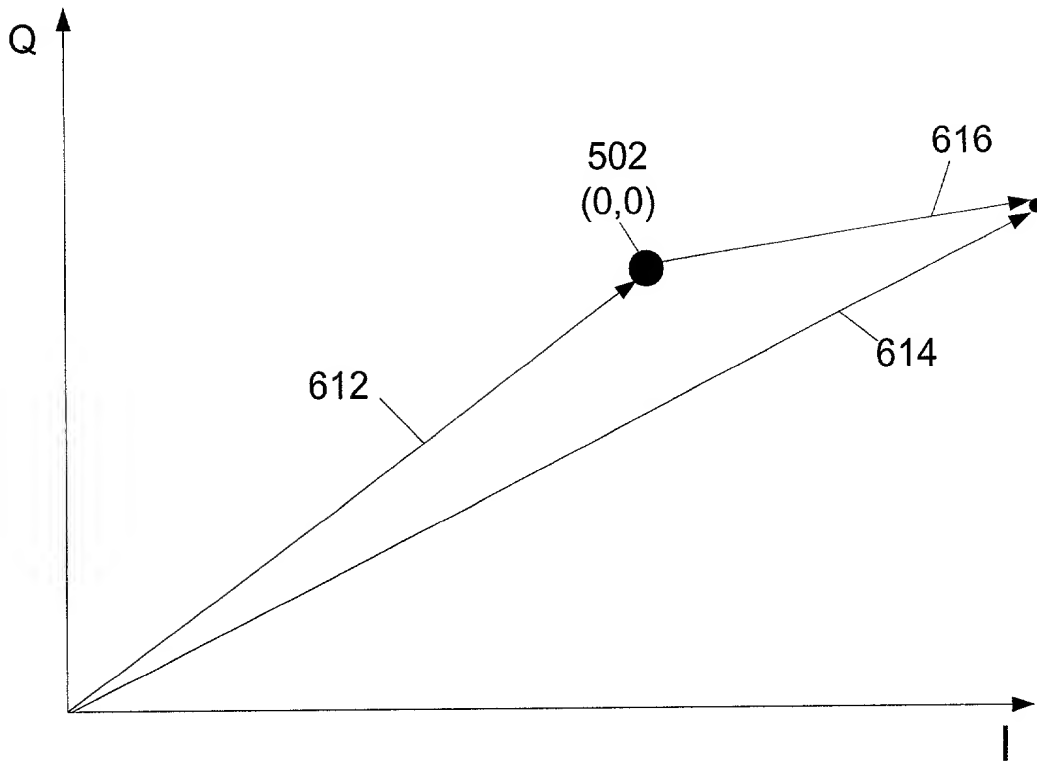


FIG. 6

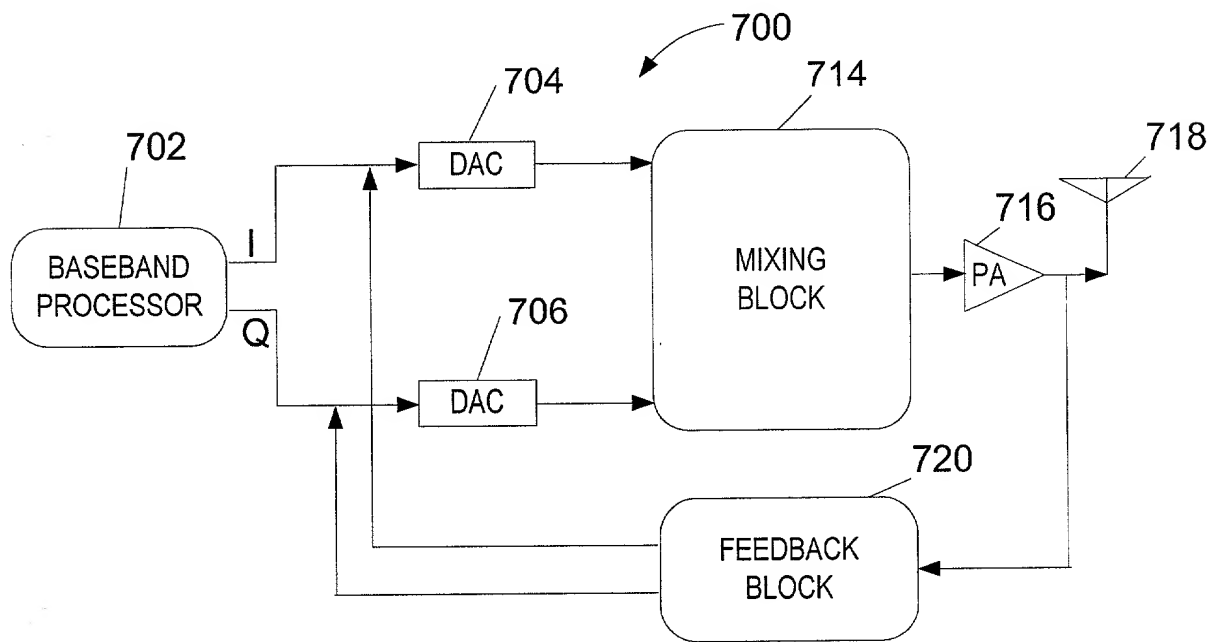


FIG. 7

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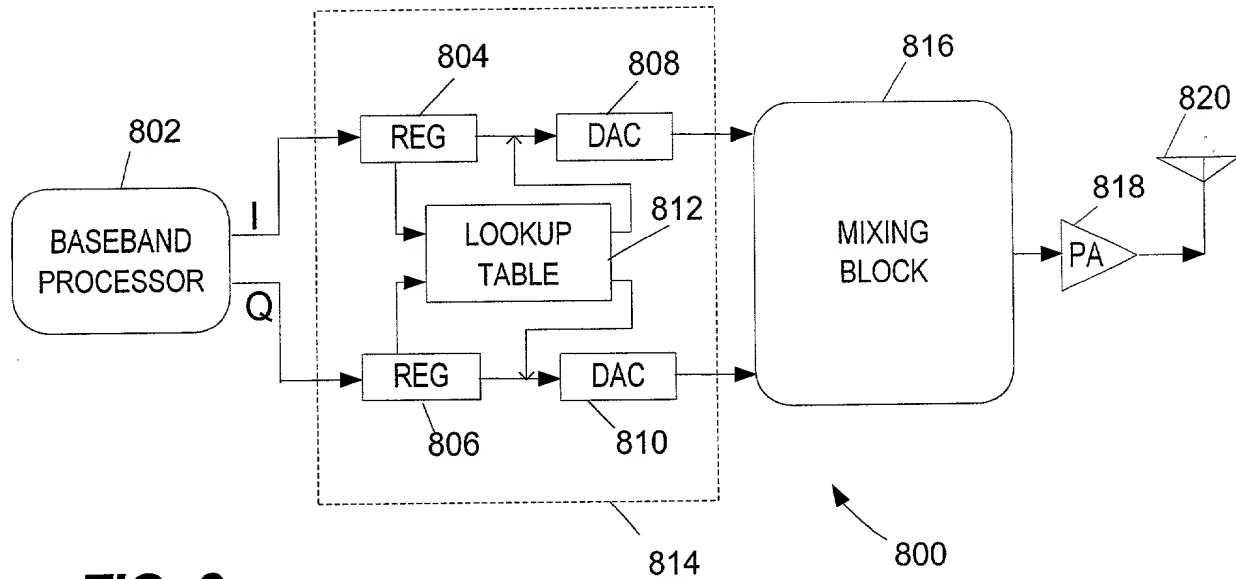


FIG. 8

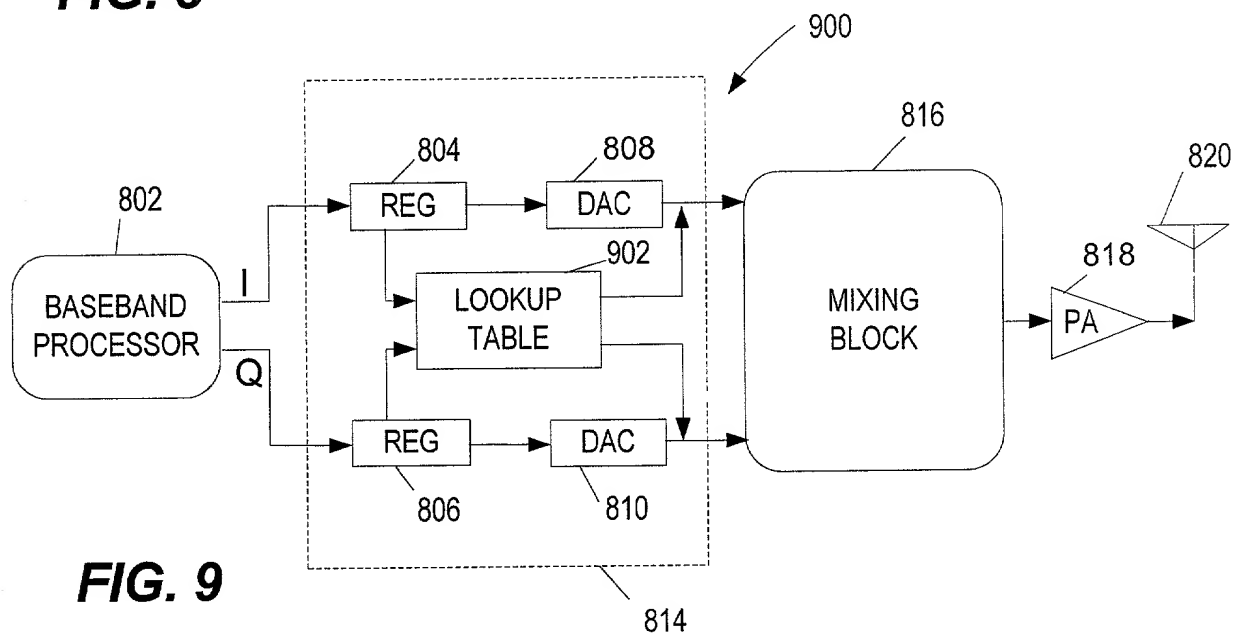


FIG. 9

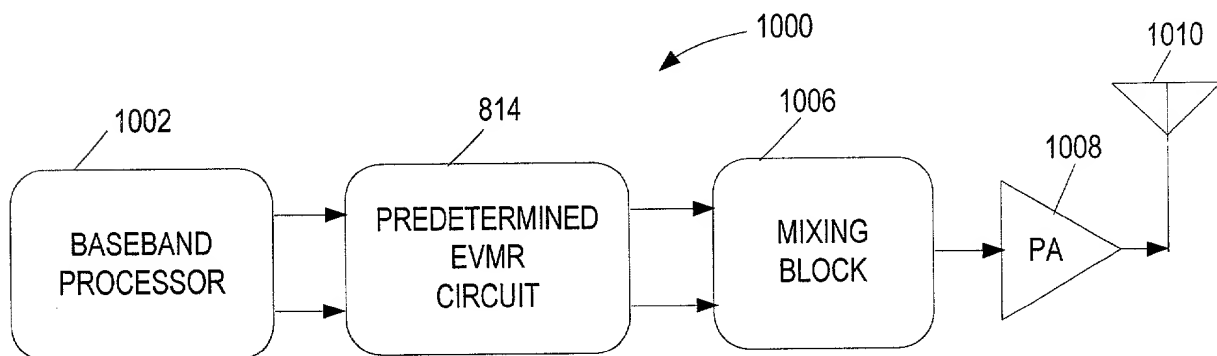


FIG. 10

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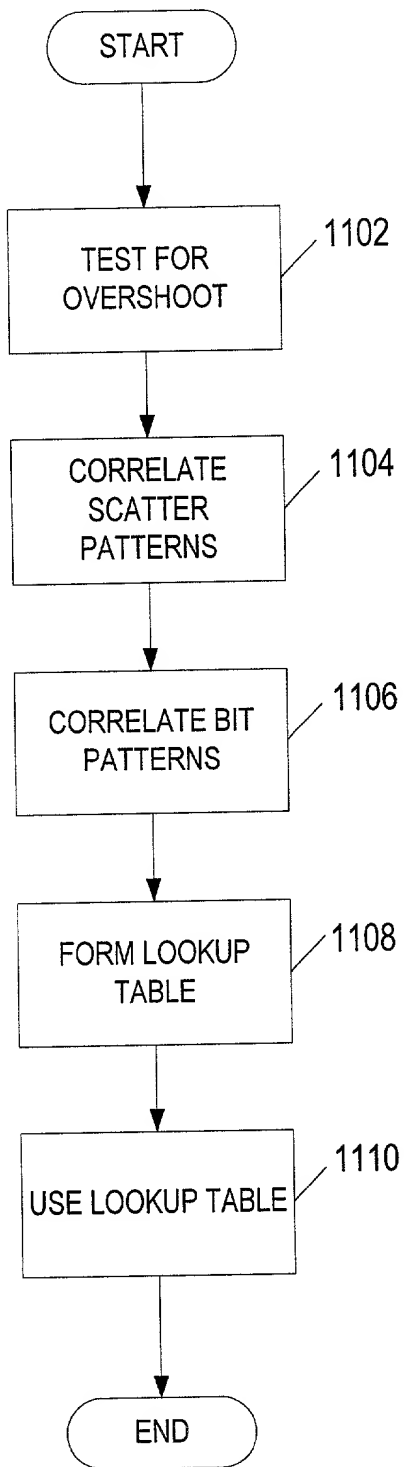


FIG. 11

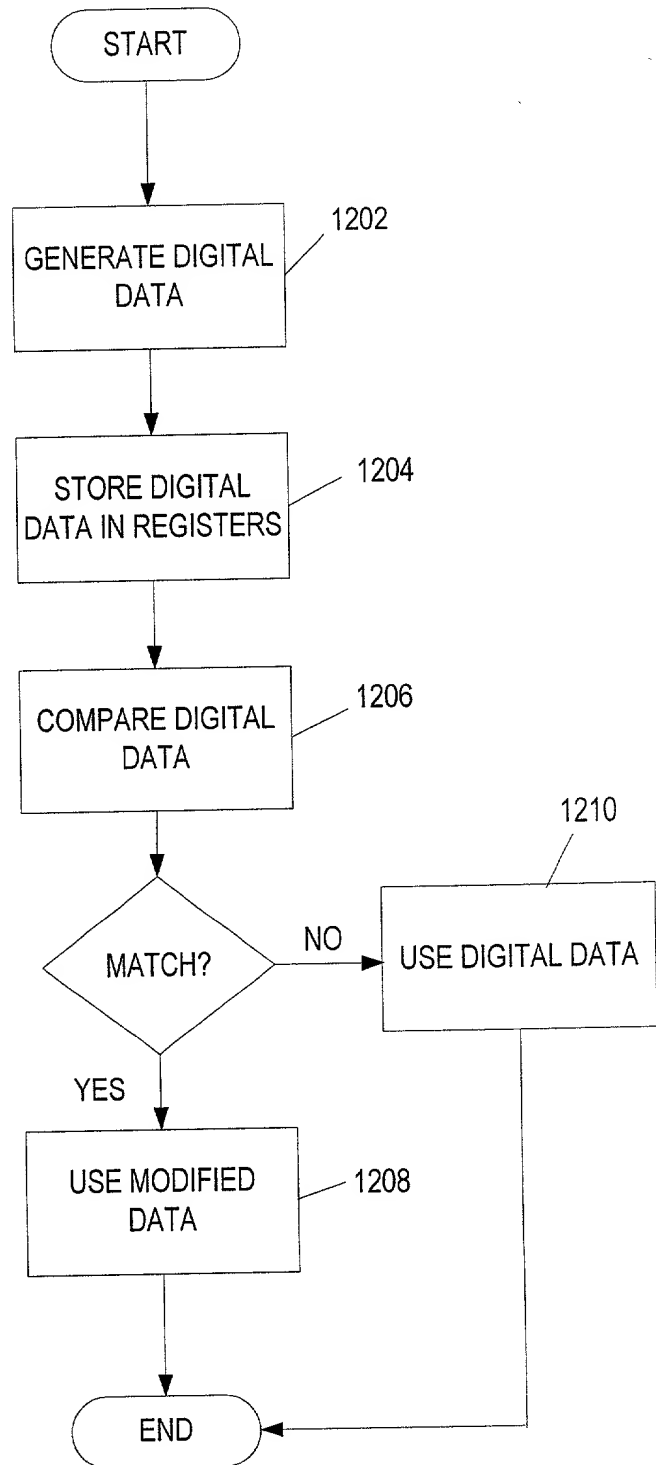


FIG. 12